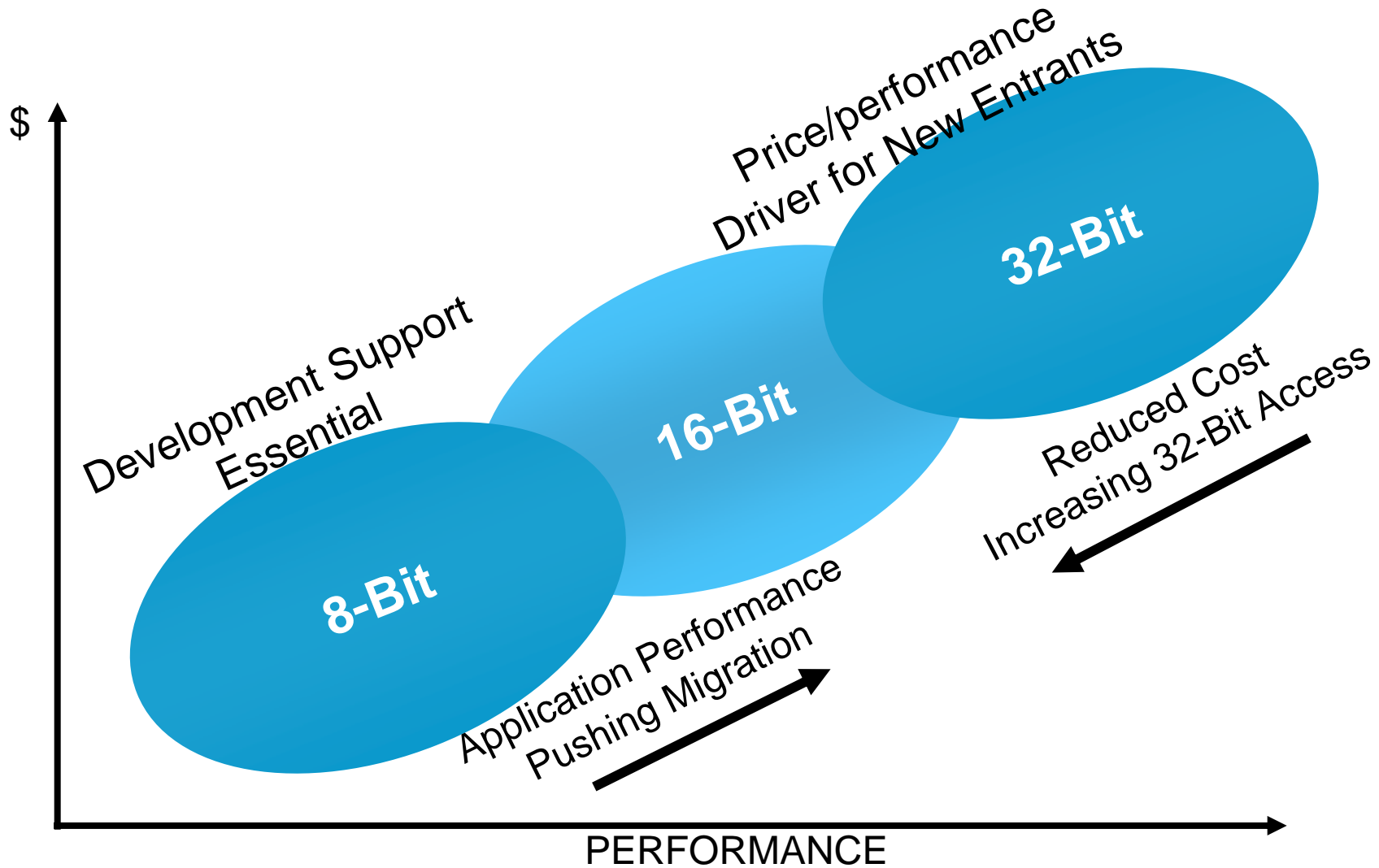
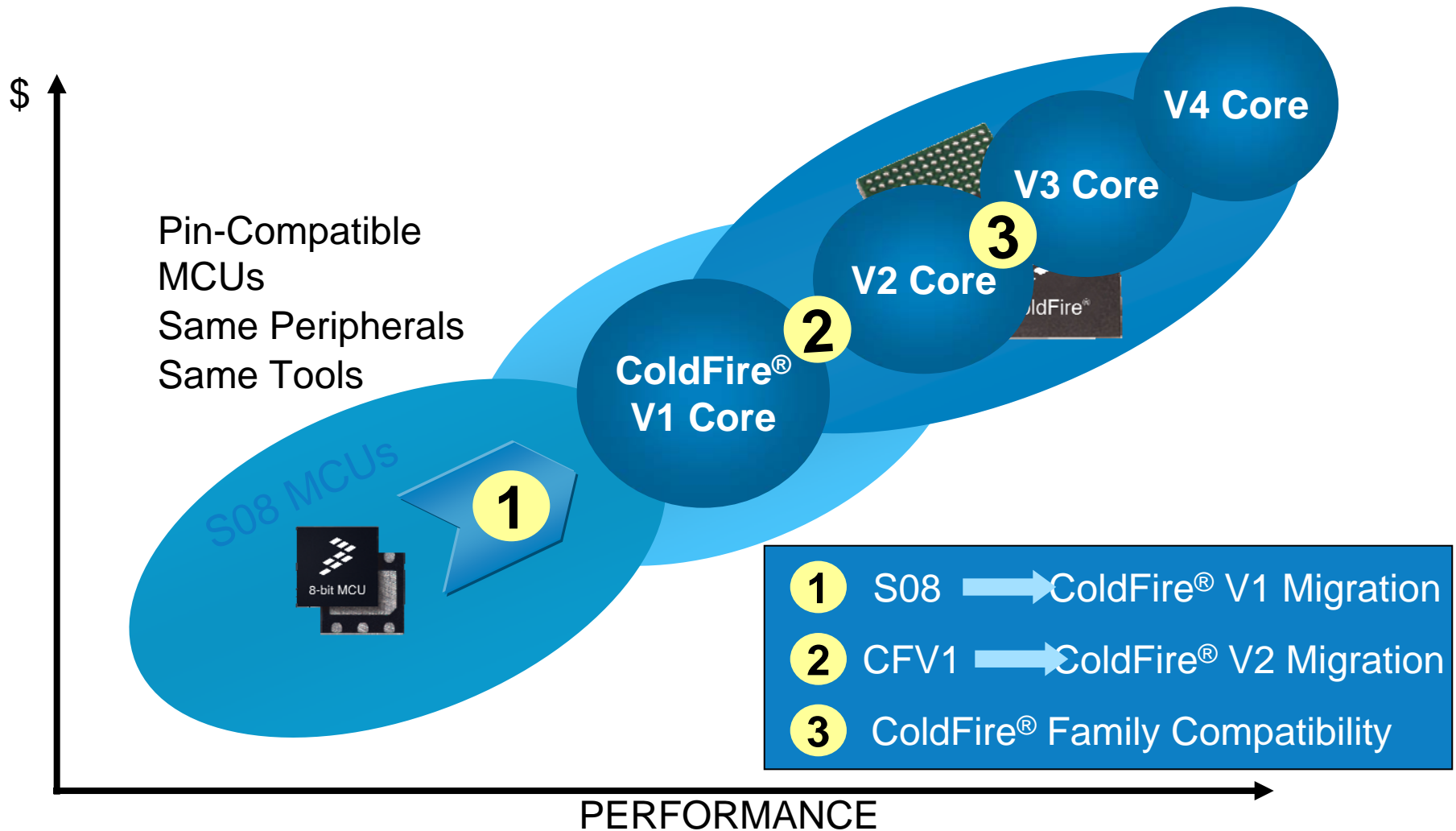


Microcontroller Landscape Is Changing



Migration to 32-bit : As Easy As 1-2-3



ColdFire® V1 Core Architecture

Instruction Set Overview

- Simplified version of the ColdFire® V2 Core
- Optional MAC/EMAC, DIV instructions
- Improved handling of byte (8-bit), word (16-bit) operands
- Same addressing modes, instruction definitions
- Provides upward compatibility to all other ColdFire® cores (V2 – V4)

Leverages Version 2 microarchitecture

- 2-stage Instruction Fetch Pipeline, 2-stage Operand Execution Pipeline
- Tightly-coupled 32-bit local memories (flash and SRAM)
- Standardized 8-bit bus to S08 peripherals
- Same programming model as ColdFire® cores (V2 – V4)
- Supports ColdFire® compiler conventions with no changes

Updated Debug Interface

- S08 single-pin BDM interface for package pin compatibility
- Implements a buffer for BDM-readable trace functionality

